

What is claimed is:

- 1 1. A memory access control device comprising:
 - 2 a memory master to make a request for access to memory;
 - 3 a memory control unit to produce control signals of memories
 - 4 based on access information to be output from said memory master;
 - 5 and
 - 6 a hit predicting unit to predict whether or not next access
 - 7 to each bank in memory becomes access to a same page;
 - 8 wherein said memory control unit, when a hit predicting unit
 - 9 predicts that "a page hit is found" which means that "next access
 - 10 to said bank becomes access to a same page", terminates its routine
 - 11 without closing a bank being presently accessed at time of
 - 12 completion of present access operations and, when said hit
 - 13 predicting unit predicts that "a miss hit is found" which means
 - 14 that "next access to said bank becomes access to a different page",
 - 15 closes said bank being presently accessed at time of completion
 - 16 of present access operations and terminates its routine.

- 1 2. The memory access control device according to Claim 1,
 - 2 wherein said hit predicting unit stores results from last "n" (n
 - 3 is a natural number) times accesses to each bank in memory as to
 - 4 whether a page hit has been found or a miss hit has been found
 - 5 and predicts, if a number of times of accesses by which a page
 - 6 hit is found out of last "n" times accesses is "m" or more (m
 - 7 $\leq n$: "m" and "n" are natural numbers), that a page hit is found
 - 8 in next access to said bank, and on the other hand predicts, if
 - 9 said number of times of accesses is not "m" or more, that a miss
 - 10 hit is found in next access to said bank.

1 3. The memory access control device according to Claim 1,
2 wherein said hit predicting unit stores results from last "j" ("j"
3 is a natural number) times accesses to each bank in memory as to
4 whether a page hit has been found or a miss hit has been found
5 and predicts, when a page hit has been found in all last "j" times
6 accesses, that a page hit is found in next access to said bank,
7 and on the other hand predicts, if no page hit has been found at
8 least one time in all last "j" times accesses, that a miss hit
9 is found in next access to said bank.

1 4. The memory access control device according to Claim 1,
2 wherein said hit predicting unit stores results from last "k" ("k"
3 is a natural number) times accesses to each bank in memory as to
4 whether a page hit has been found or a miss hit has been found
5 and predicts, if a miss hit has been found in all last "k" times
6 accesses, that a miss hit is found in next access to said bank,
7 and on the other hand predicts, if no miss hit has been found at
8 least one time in all last "k" times accesses, that a miss hit
9 is found in next access to said bank.

1 5. The memory access control device according to Claim 1,
2 wherein said hit predicting unit stores results from last "n" ("n"
3 is a natural number) times accesses to each bank in memory as to
4 whether a page hit has been found or a miss hit has been found
5 and predicts, when a miss hit has been always found in all last
6 "k" times accesses ($k \leq n$: "k" and "n" each are a natural number)
7 out of last "n" times accesses, that a miss hit is found in next
8 access to said bank and predicts, when no miss hit has been found
9 at least one time in all last "k" times accesses out of last "n"

10 times accesses, if a page hit has been always found in all last
11 "j" times accesses ($j \leq n$: "j" and "n" each are a natural number)
12 out of last "n" times accesses, that a page hit is found in next
13 access to said bank and predicts, when a miss hit has been found
14 at least one time in all last "j" times accesses out of last "n"
15 times accesses, if a number of times of accesses by which a page
16 hit has been found out of last "n" times accesses is "m" times
17 or more ($m \leq n$: "m" and "n" each are a natural number), that a page
18 hit is found in next access to said bank and predicts, when a number
19 of times of accesses by which a page hit has been found out of
20 last "n" times accesses is not "m" times or more, that a miss hit
21 is found in next access to said bank.

1 6. The memory access control device according to Claim 1,
2 wherein said memory master informs, when a bank and a page to be
3 accessed next have been determined, said memory control unit of
4 information about said bank and said page to be accessed and
5 wherein said memory control unit, if said bank to be accessed next
6 by said memory master is same as that being presently accessed
7 and said page to be accessed by said memory master is same as that
8 being presently accessed, terminates its routine, regardless of
9 a prediction result from said hit predicting unit, without closing
10 said bank being presently accessed at time of completion of
11 present access operations and, if said bank to be accessed next
12 by said memory master is same as that being presently accessed
13 and said page to be accessed by said memory master is different
14 from that being presently accessed, closes said bank being
15 presently accessed at time of completion of present access
16 operations, regardless of a prediction result from said hit

17 predicting unit, and terminates its routine.

1 7. The memory access control device according to Claim 2,
2 wherein said memory master informs, when a bank and a page to be
3 accessed next have been determined, said memory control unit of
4 information about said bank and said page to be accessed and
5 wherein said memory control unit, if said bank to be accessed next
6 by said memory master is same as that being presently accessed
7 and said page to be accessed by said memory master is same as that
8 being presently accessed, terminates its routine, regardless of
9 a prediction result from said hit predicting unit, without closing
10 said bank being presently accessed at time of completion of
11 present access operations and, if said bank to be accessed next
12 by said memory master is same as that being presently accessed
13 and said page to be accessed by said memory master is different
14 from that being presently accessed, closes said bank being
15 presently accessed at time of completion of present access
16 operations, regardless of a prediction result from said hit
17 predicting unit, and terminates its routine.

1 8. The memory access control device according to Claim 3,
2 wherein said memory master informs, when a bank and a page to be
3 accessed next have been determined, said memory control unit of
4 information about said bank and said page to be accessed and
5 wherein said memory control unit, if said bank to be accessed next
6 by said memory master is same as that being presently accessed
7 and said page to be accessed by said memory master is same as that
8 being presently accessed, terminates its routine, regardless of
9 a prediction result from said hit predicting unit, without closing

10 said bank being presently accessed at time of completion of
11 present access operations and, if said bank to be accessed next
12 by said memory master is same as that being presently accessed
13 and said page to be accessed by said memory master is different
14 from that being presently accessed, closes said bank being
15 presently accessed at time of completion of present access
16 operations, regardless of a prediction result from said hit
17 predicting unit, and terminates its routine.

1 9. The memory access control device according to Claim 4,
2 wherein said memory master informs, when a bank and a page to be
3 accessed next have been determined, said memory control unit of
4 information about said bank and said page to be accessed and
5 wherein said memory control unit, if said bank to be accessed next
6 by said memory master is same as that being presently accessed
7 and said page to be accessed by said memory master is same as that
8 being presently accessed, terminates its routine, regardless of
9 a prediction result from said hit predicting unit, without closing
10 said bank being presently accessed at time of completion of
11 present access operations and, if said bank to be accessed next
12 by said memory master is same as that being presently accessed
13 and said page to be accessed by said memory master is different
14 from that being presently accessed, closes said bank being
15 presently accessed at time of completion of present access
16 operations, regardless of a prediction result from said hit
17 predicting unit, and terminates its routine.

1 10. The memory access control device according to Claim 5,
2 wherein said memory master informs, when a bank and a page to be

3 accessed next have been determined, said memory control unit of
4 information about said bank and said page to be accessed and
5 wherein said memory control unit, if said bank to be accessed next
6 by said memory master is same as that being presently accessed
7 and said page to be accessed by said memory master is same as that
8 being presently accessed, terminates its routine, regardless of
9 a prediction result from said hit predicting unit, without closing
10 said bank being presently accessed at time of completion of
11 present access operations and, if said bank to be accessed next
12 by said memory master is same as that being presently accessed
13 and said page to be accessed by said memory master is different
14 from that being presently accessed, closes said bank being
15 presently accessed at time of completion of present access
16 operations, regardless of a prediction result from said hit
17 predicting unit, and terminates its routine.

1 11. A memory access control device comprising:
2 two or more memory masters to make a request for access to
3 memory;
4 an arbiter unit to arbitrate memory access requests fed from
5 said memory masters and to select access information fed from any
6 one of said memory masters;
7 a memory control unit to produce a control signal of memory
8 based on access information output from said arbiter unit; and
9 a hit predicting unit to predict whether or not next access
10 to each bank in memory becomes access to a same page;
11 wherein said memory control unit, when said hit predicting
12 unit predicts that "a page hit is found" which means that "next
13 access to said bank is access to a same page", terminates its

14 routine without closing said bank being presently accessed at time
15 of completion of present access operations and when said hit
16 predicting unit predicts that "a miss hit is found" which means
17 that "next access to said bank is access to a different page",
18 closes said bank being presently accessed at time of present
19 access operations and terminates its routine.

1 12. The memory access control unit according to Claim 11,
2 wherein said hit predicting unit stores results from last "n" ("n"
3 is a natural number) times accesses to each bank in memory as to
4 whether a page hit has been found or a miss hit has been found
5 and predicts, if a number of times of accesses by which a page
6 hit is found out of last "n" times accesses is "m" or more (m
7 $\leq n$: "m" and "n" each are a natural number), that a page hit is
8 found in next access to said bank and predicts, if said number
9 of times of accesses is not "m" or more, that a miss hit is found
10 in next access to said bank.

1 13. The memory access control device according to Claim 11,
2 wherein said hit predicting unit stores results from last "j" ("j"
3 is a natural number) times accesses to each bank in memory as to
4 whether a page hit has been found or a miss hit has been found
5 and predicts, when a page hit has been found in all last "j" times
6 accesses, that a page hit is found in next access to said bank
7 and predicts, if no page hit has been found in all last "j" times
8 accesses, that a miss hit is found in next access to said bank.

1 14. The memory access control device according to Claim 11,
2 wherein said hit predicting unit stores results from last "k" ("k"

3 is a natural number) times accesses to each bank in memory as to
 4 whether a page hit has been found or a miss hit has been found
 5 and predicts, if a miss hit has been found in all last "k" times
 6 accesses, that a miss hit is found in next access to said bank
 7 and predicts, if no miss hit has been found in all last "k" times
 8 accesses, that a page hit is found in next access to said bank.

1 15. The memory access control device according to Claim 11,
 2 wherein said hit predicting unit stores results from last "n" ("n"
 3 is a natural number) times accesses to each bank in memory as to
 4 whether a page hit has been found or a miss hit has been found
 5 and predicts, when a miss hit has been found in all last "k" (k
 6 $\leq n$: "k" and "n" each are a natural number) times accesses out
 7 of last "n" times accesses, that a miss hit is found in next access
 8 to said bank and predicts, when no miss hit has been found at least
 9 one time in all last "k" times accesses out of last "n" times
 10 accesses, if a page hit is found in all last "j" times accesses
 11 ($j \leq n$: "j" and "n" each are a natural number) out of last "n" times
 12 accesses, that a page hit is found in next access to said bank
 13 and predicts, when a miss hit has been found at least one time
 14 in all last "j" times accesses out of last "n" times accesses,
 15 if a number of times of accesses by which a page hit has been found
 16 out of last "n" times accesses is "m" times or more ($m \leq n$: "m"
 17 and "n" each are a natural number), that a page hit is found in
 18 next access to said bank and predicts, when a number of times of
 19 accesses by which a page hit has been found out of last "n" times
 20 accesses is not "m" times or more, that a miss hit is found in
 21 next access to said bank.

1 16. The memory access control device according to Claim 11,
2 wherein each memory master informs, when a bank and a page to be
3 accessed next have been determined, said arbiter unit and said
4 memory control unit of information about said bank and said page
5 to be accessed and wherein said memory control unit, if there
6 exists said memory master which gets next access to a same bank
7 as that being presently accessed and to a same page as that being
8 presently accessed, terminates its routine without closing said
9 bank being presently accessed at time of completion of present
10 access operations, regardless of a prediction result fed from said
11 hit predicting unit, and terminates its routine and, if there
12 exists said memory master which gets next access to a same bank
13 as that being presently accessed and to a page being different
14 from that being presently accessed, closes said bank being
15 presently accessed at time of completion of present access
16 operations, regardless of a prediction result fed from a hit
17 predicting unit, and terminates its routine and said arbiter unit,
18 if there exists said memory master which gets next access to a
19 same bank and a same page as those being presently accessed,
20 selects said memory master with priority.

1 17. The memory access control device according to Claim 12,
2 wherein each memory master informs, when a bank and a page to be
3 accessed next have been determined, said arbiter unit and said
4 memory control unit of information about said bank and said page
5 to be accessed and wherein said memory control unit, if there
6 exists said memory master which gets next access to a same bank
7 as that being presently accessed and to a same page as that being
8 presently accessed, terminates its routine without closing said

9 bank being presently accessed at time of completion of present
10 access operations, regardless of a prediction result fed from said
11 hit predicting unit, and terminates its routine and, if there
12 exists said memory master which gets next access to a same bank
13 as that being presently accessed and to a page being different
14 from that being presently accessed, closes said bank being
15 presently accessed at time of completion of present access
16 operations, regardless of a prediction result fed from a hit
17 predicting unit, and terminates its routine and said arbiter unit,
18 if there exists said memory master which gets next access to a
19 same bank and a same page as those being presently accessed,
20 selects said memory master with priority.

1 18. The memory access control device according to Claim 13,
2 wherein each memory master informs, when a bank and a page to be
3 accessed next have been determined, said arbiter unit and said
4 memory control unit of information about said bank and said page
5 to be accessed and wherein said memory control unit, if there
6 exists said memory master which gets next access to a same bank
7 as that being presently accessed and to a same page as that being
8 presently accessed, terminates its routine without closing said
9 bank being presently accessed at time of completion of present
10 access operations, regardless of a prediction result fed from said
11 hit predicting unit, and terminates its routine and, if there
12 exists said memory master which gets next access to a same bank
13 as that being presently accessed and to a page being different
14 from that being presently accessed, closes said bank being
15 presently accessed at time of completion of present access
16 operations, regardless of a prediction result fed from a hit

17 predicting unit, and terminates its routine and said arbiter unit,
18 if there exists said memory master which gets next access to a
19 same bank and a same page as those being presently accessed,
20 selects said memory master with priority.

1 19. The memory access control device according to Claim 14,
2 wherein each memory master informs, when a bank and a page to be
3 accessed next have been determined, said arbiter unit and said
4 memory control unit of information about said bank and said page
5 to be accessed and wherein said memory control unit, if there
6 exists said memory master which gets next access to a same bank
7 as that being presently accessed and to a same page as that being
8 presently accessed, terminates its routine without closing said
9 bank being presently accessed at time of completion of present
10 access operations, regardless of a prediction result fed from said
11 hit predicting unit, and terminates its routine and, if there
12 exists said memory master which gets next access to a same bank
13 as that being presently accessed and to a page being different
14 from that being presently accessed, closes said bank being
15 presently accessed at time of completion of present access
16 operations, regardless of a prediction result fed from a hit
17 predicting unit, and terminates its routine and said arbiter unit,
18 if there exists said memory master which gets next access to a
19 same bank and a same page as those being presently accessed,
20 selects said memory master with priority.

1 20. The memory access control device according to Claim 15,
2 wherein each memory master informs, when a bank and a page to be
3 accessed next have been determined, said arbiter unit and said

4 memory control unit of information about said bank and said page
5 to be accessed and wherein said memory control unit, if there
6 exists said memory master which gets next access to a same bank
7 as that being presently accessed and to a same page as that being
8 presently accessed, terminates its routine without closing said
9 bank being presently accessed at time of completion of present
10 access operations, regardless of a prediction result fed from said
11 hit predicting unit, and terminates its routine and, if there
12 exists said memory master which gets next access to a same bank
13 as that being presently accessed and to a page being different
14 from that being presently accessed, closes said bank being
15 presently accessed at time of completion of present access
16 operations, regardless of a prediction result fed from a hit
17 predicting unit, and terminates its routine and said arbiter unit,
18 if there exists said memory master which gets next access to a
19 same bank and a same page as those being presently accessed,
20 selects said memory master with priority.